UNIVERSITY OF KERALA

TIME TABLE FOR THE SIXTH SEMESTER B.TECH DEGREE (2008 SCHEME) EXAMINATION, JANUARY 2023

(Supplementary (2012 Admission)/Part Time/Mercy Chance (2008 Admission, 2009 Admission, 2010 Admission and 2011 Admission) including 2003 Scheme transitory candidates)

DATE	DAY	TIME	SUBJECT
16/05/2023	TUESDAY	1.30pm-4.30pm	Design of Steel Structures (C) Metrology & Instrumentation (MP) Mass Transfer Operations (B) Vehicle Maintenance (U) Electrical Machines III (E) Microcontroller Based System Design (TA) Compiler Design (RF)
18/05/2023	THURSDAY	1.30pm-4.30pm	Geo-technical Engineering –I (C) Dynamics of Machinery(MP) Transport Phenomena in Bioprocesses (B) Microprocessors & Applications (E) Principles of Programming Languages (R) Computer Networks (F)
22/05/2023	MONDAY	1.30pm-4.30pm	Water Resources Engineering (C) Computer Aided Design(MPU) Process Dynamics and Control (B) Numerical Techniques & Computer Programming (E) Control Systems (T) Formal Languages and Automata Theory (R) Software Architecture (F)

24/05/2023	WEDNESDAY	1.30pm-4.30pm	Transportation Engineering II (C) Heat & Mass Transfer (MU) Numerical Methods for Process Engineers (BH) Industrial Engineering & Management (E) Digital Communication (T) Digital Signal Processing (R) Internet Technology (F)
26/05/2023	FRIDAY	2.00pm-5.00pm	Programming and Numerical Methods (C) Design of Machine Elements -I (M) Mechanics of Machinery (U) Power System Engineering II (E) Antenna & Wave Propagation (T) High Performance Microprocessors (R) Computer Graphics (F)
29/05/2023	MONDAY	1.30pm-4.30pm	**Elective II – [C]** • Soil Exploration (C) • Wave Hydro dynamics (C) • Traffic Engineering (C) • Hydrology & Water Resources (C) • Valuation of Real Properties (C) **Elective II- [M]** • New Energy Systems (M) • Total Quality Management (M) **Elective II-[E]** • Biomedical Instrumentation (E) **Elective II-[T] or [TA]** • Digital Image Processing (TA) • Electronic Instrumentation (T) • Optimization Techniques (TA) Data communication (R) Embedded Systems (F)
31/05/2023	WEDNESDAY	1.30pm -4.30pm	VLSI Design (TA)

University Buildings,
Thiruvananthapuram
Date :09.05.2023

Sd/-
CONTROLLER OF EXAMINATIONS